

ABSTRACT OF THE DISCLOSURE

An array substrate for a liquid crystal display device includes a gate line, a data line including a first data line having a first width and a second data line having a second width overlying the first data line, the second width is larger than the first width, a pixel electrode in a pixel region and defined by a crossing of the gate line and the data line, the pixel electrode being formed during a same process as the second data line, and a thin film transistor connected to the pixel electrode.

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